**Figure 1**

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The diagram illustrates the internal architecture of a server (200). At the top, two processors, **Processor 202** and **Processor 204**, are connected to a horizontal **System Bus 206**. Below the system bus is a central block containing the **Memory Controller/Cache 208** and the **I/O Bridge 210**. **Local Memory 209** is connected to the memory controller/cache. The I/O bridge 210 is connected to a vertical **I/O Bus 212**. Various peripheral components are connected to this I/O bus: **Graphics Adapter 230** and **Hard Disk 232** are connected to the left side, while three **PCI Bus Bridge** units (**214**, **222**, and **224**) are connected to the right side. Each PCI bus bridge is connected to an external **PCI Bus**: **PCI Bus Bridge 214** connects to **PCI Bus 216**, which is also connected to a **Modem 218** and a **Network Adapter 220**; **PCI Bus Bridge 222** connects to **PCI Bus 226**; and **PCI Bus Bridge 224** connects to **PCI Bus 228**. The entire assembly is labeled **200 Server** at the bottom.

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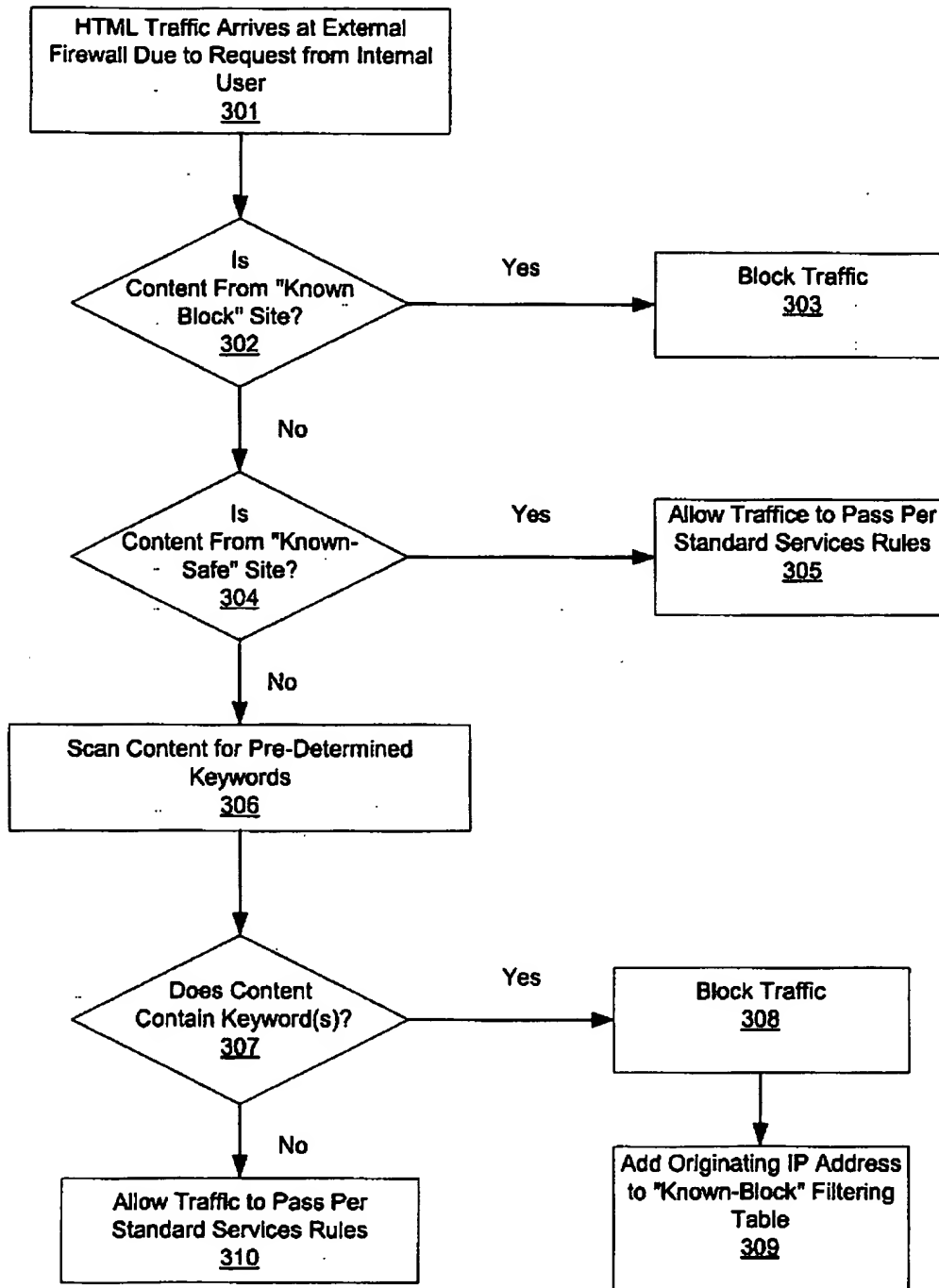


Figure 3

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